## GALLIUM SEMICONDUCTOR

## GTH2e-2425300P

## 50V,2.4- 2.5GHz, 300W GaN HEMT

#### **FEATURES**

Operating Frequency Range: 2.4 – 2.5 GHz

• Operating Drain Voltage: 50V

Maximum Output Power (Psat): 300W

Air Cavity Plastic Package (ACP)

Input internally pre-matched F0 + 2F0

Suitable for CW applications



ACP-800 4L Air Cavity Plastic

#### **DESCRIPTION**

The GTH2e-2425300P is a 300W (P3dB) pre-matched discrete GaN-on-SiC HEMT which operates from 2.4 to 2.5 GHz on a 50V supply rail. The wide bandwidth of the GTH2e-2425300P makes it suitable for Industrial Scientific Medical, RF Energy and CW operations.

The device is housed in an industry-standard ACP-800 4L Air Cavity Plastic package. Lead-free and RoHS compliant.

Typical Performances 1 Tone pulsed CW (10% duty cycle, 100µs width),

- (1) Optimum Peak Power at 2.5dB in compression in Class AB Bias
- (2) Optimum Peak Efficiency at 2.5dB in compression in Class AB Bias
- (3) Optimum Peak Power at 2.5dB in compression in Class C Bias
- (4) Optimum Peak Efficiency at 2.5dB in compression in Class C Bias

For 1 section of the device, Vds=50V, TA = 25°C

| Frequency<br>(MHz) | Pout<br>(dBm)       | Pout<br>(Watt)     | Gain<br>(dB)        | Eff<br>(%)          |
|--------------------|---------------------|--------------------|---------------------|---------------------|
| 2400               | 54.0 <sup>(1)</sup> | 253 <sup>(1)</sup> | 17.1 <sup>(2)</sup> | 70.0(2)             |
| 2500               | 54.3 <sup>(1)</sup> | 268 <sup>(1)</sup> | 17.3 <sup>(2)</sup> | 70.8(2)             |
| 2500               | 54.2 <sup>(3)</sup> | 260 <sup>(3)</sup> | 14.9 <sup>(4)</sup> | 76.3 <sup>(4)</sup> |



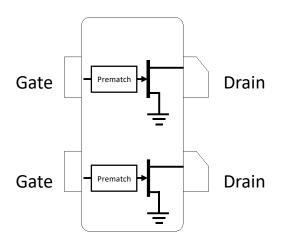
# 50V,2.4- 2.5GHz, 300W GaN HEMT

## **ABSOLUTE MAXIMUM RATINGS**(1, 2)

| Parameter                  | Rating      | Symbols and Units         |
|----------------------------|-------------|---------------------------|
| Drain Source Voltage       | 150         | V <sub>DS</sub> (V)       |
| Gate Source Voltage        | -8 to +2    | V <sub>GS</sub> (V)       |
| Operating Voltage          | 55          | V <sub>dsq</sub> (V)      |
| Junction Temperature       | +225        | T <sub>JUNC</sub> (°C)    |
| Storage Temperature        | -65 to +150 | T <sub>STORAGE</sub> (°C) |
| Case Operating Temperature | -40 to +105 | T <sub>CASE</sub> (°C)    |

- 1. Exceeding any of these limits may cause permanent damage to this device or seriously limit the life time (MTTF)
- 2. GalliumSemi does not recommend sustained operation above maximum operating conditions.

## **BLOCK DIAGRAM**



## **ELECTRICAL SPECIFICATIONS: TA = 25°C**

| Parameter                         | Min. | Тур. | Max. | Symbols and<br>Units  | Test conditions        |
|-----------------------------------|------|------|------|-----------------------|------------------------|
| Frequency Range                   | 2400 |      | 2500 | MHz                   |                        |
| DC Characteristics                |      |      |      |                       |                        |
| Drain Source Breakdown<br>Voltage | 150  |      |      | V <sub>BDSS</sub> (V) |                        |
| Drain Source Leakage Current      |      | tbd  |      | I <sub>DLK</sub> (mA) | Vgs = -8V, $Vds = 50V$ |
| Gate Threshold Voltage            | -3.4 |      | -1.5 | V <sub>GS</sub> (V)   | Vds = 50V              |
| Operating Conditions              |      |      |      |                       |                        |
| Gate Bias Voltage                 |      | -2.5 |      | V <sub>GSQ</sub> (V)  |                        |
| Drain Voltage                     |      | 50   |      | V <sub>DSQ</sub> (V)  |                        |
| Quiescent Drain Current           |      | 600  |      | I <sub>DQ</sub> (mA)  |                        |
|                                   |      |      |      |                       |                        |

# 50V,2.4- 2.5GHz, 300W GaN HEMT

# RF ELECTRICAL SPECIFICATIONS: $T_A = 25^{\circ}C$ , VDS = 50 V,IDQ = 100 mA, Freq= 2470MHz Note: Performance<sup>(1)</sup> in GalliumSemi Production Test Fixture, 50 $\Omega$ system

| Parameter                  | Symbol              | Min.                    | Тур. | Max.  | Units | Notes                               |  |
|----------------------------|---------------------|-------------------------|------|-------|-------|-------------------------------------|--|
| Small Signal Gain          | Gss                 |                         | 17   |       | dB    |                                     |  |
| Power Gain                 | G <sub>SAT</sub>    |                         | 14   |       | dB    | Pulse (100 μsec,                    |  |
| Saturated Drain Efficiency | DEff <sub>SAT</sub> |                         | 75   |       | %     | — 10% Duty Cycle)                   |  |
| Saturated Output Power     | P <sub>SAT</sub>    |                         | 55   |       | dBm   |                                     |  |
| Ruggedness Output          |                     |                         |      |       |       | Pulse (100 μsec,<br>20% Duty Cycle) |  |
| nismatch                   | Ψ                   | VSWR = 20:1, all angles |      | ngles |       | No damage or shift in performances  |  |

<sup>1. 1</sup> Tone Pulse CW, pulse width 100us, duty cycle 20%

## 50V,2.4- 2.5GHz, 300W GaN HEMT

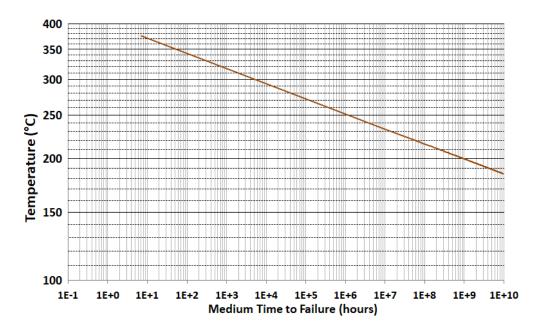
## THERMAL AND RELABILITY INFORMATION -CW (1, 2): T<sub>c</sub> = 85°C

## Rth(°C/W)= TBD

| Parameter                | Test condition | Value  | Units |
|--------------------------|----------------|--------|-------|
| Channel Temperature, Tch | _              | 172    | °C    |
| Rth                      | 129 W          | 0.67   | °C/W  |
| MTTF                     |                | > 1E10 | Hrs   |

<sup>1.</sup>Using 5um thermal grease - 4W/m-K.

<sup>2.</sup>Thermal Resistance using Finite Element Analysis (FEA) simulation, calibrated with Infrared measurement on surface temperature.



## 50V,2.4- 2.5GHz, 300W GaN HEMT

## LOADPULL MEASUREMENT FOR 1 SECTION OF THE DEVICE,

Typical Performances 1 Tone pulsed CW (10% duty cycle, 100µs width)

- (1) 50V, Class AB Bias
- (2) 50V, Class C Bias

|          | For Optimum Peak Power @ 2.5dB Compression |                           |                    |                     |                     |                   |  |
|----------|--|---------------------------|--------------------|---------------------|---------------------|-------------------|--|
| Freq-MHz | Zin_F0                                     | ZI_F0                     | Pout_W             | Pout-dBm            | Gain_dB             | Eff-%             |  |
| 2400     | 15.4 – 0.4j <sup>(1)</sup>                 | 2.1 – 2.8j <sup>(1)</sup> | 253 <sup>(1)</sup> | 54.0 <sup>(1)</sup> | 15.8 <sup>(1)</sup> | 64 <sup>(1)</sup> |  |
| 2500     | 7.4 - 1.5j <sup>(1)</sup>                  | 1.8 – 3.7j <sup>(1)</sup> | 266 <sup>(1)</sup> | 54.3 <sup>(1)</sup> | 15.4 <sup>(1)</sup> | 62 <sup>(1)</sup> |  |
| 2500     | 17.1 + 3.7j <sup>(2)</sup>                 | $2.2 - 4.2j^{(2)}$        | 261 <sup>(2)</sup> | 54.2 <sup>(2)</sup> | 13.3(2)             | 68(2)             |  |

## For Optimum Peak Efficiency @ 2.5dB Compression

| Freq-MHz | Zin_F0                     | ZI_F0                     | Pout_W             | Pout-dBm            | Gain_dB             | Eff-%               |
|----------|----------------------------|---------------------------|--------------------|---------------------|---------------------|---------------------|
| 2400     | 15.4 – 0.4j <sup>(1)</sup> | $2.0 - 1.8j^{(1)}$        | 173 <sup>(1)</sup> | 52.3 <sup>(1)</sup> | 17.1 <sup>(1)</sup> | 70.0 <sup>(1)</sup> |
| 2500     | 4.6 - 1.0j <sup>(1)</sup>  | $2.1 - 2.1j^{(1)}$        | 174 <sup>(1)</sup> | 52.4 <sup>(1)</sup> | 17.3 <sup>(1)</sup> | 70.8(1)             |
| 2500     | 13.6 – 3.1j <sup>(2)</sup> | 1.9 – 2.5j <sup>(2)</sup> | 198 <sup>(2)</sup> | 53.0 <sup>(2)</sup> | 14.9 <sup>(2)</sup> | 76.3 <sup>(2)</sup> |

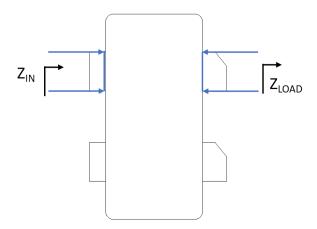
#### LOADPULL MEASUREMENT NOTES

Source is internally Prematched in the package and therefore non sensitive to external matching, Load impedance @ 2nd Harmonic are set to 10 Ohms.

Z<sub>LOAD</sub>: Measured Impedance presented to the output of the device in the reference plane

Z<sub>IN</sub>: Measured input Impedance at the input of the device in the reference plane

## Impedance Reference Plane



Raw data and full Loadpull measurement report available at request: <a href="mailto:sales@galliumsemi.com">sales@galliumsemi.com</a>



# 50V,2.4- 2.5GHz, 300W GaN HEMT

#### **GAN HEMT BIASING SEQUENCE**

## To turn the transistor ON

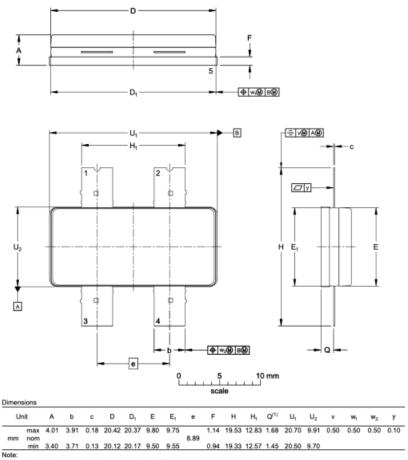
- 1. Set V<sub>GS</sub> to -5V
- 2. Turn on V<sub>DS</sub> to normal operation voltage (50V)
- 3. Slowly increase V<sub>GS</sub> to set I<sub>DQ</sub> current to target value.
- 4. Apply RF power

## To turn the transistor OFF

- 1. Turn the RF power off
- 2. Decrease V<sub>GS</sub> to -5V
- 3. Turn off V<sub>D.</sub> Wait a few seconds for drain capacitor to discharge
- 4. Turn off V<sub>GS</sub>

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## **PACKAGE DIMENSIONS**



## **PIN CONFIGURATION**

| Pin        | Input/Output              |
|------------|---------------------------|
| 1, 2       | RF Output / Drain Voltage |
| 3, 4       | RF Input / Gate Voltage   |
| 5 (flange) | Ground                    |

## **DEVICE LABEL**

| Line 1: | COMPANY NAME: GALLIUM |                    |  |  |  |
|---------|-----------------------|--------------------|--|--|--|
| Line 2: | PART NUMBER - WAFER # |                    |  |  |  |
| Line 3: | AA:                   | Assembly Code      |  |  |  |
|         | YYWW:                 | Assembly Date Code |  |  |  |
|         | R:                    | Reserved code      |  |  |  |

<sup>1.</sup> Dimension Q is measured at 0.1 mm away from the flange.
2. Ringframe and/or ringframe glue shall not overhang at the side of the flange.



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## HANDLING PRECAUTIONS

| Parameter                           | Symbol | Class             | Test Methodology                |
|-------------------------------------|--------|-------------------|---------------------------------|
| ESD-Human Body Model                | HBM    | Class 1A (250 V)  | ANSI/ESDA/JEDEC Standard JS-001 |
| ESD - Charged Device Model          | CDM    | Class C3 (1500 V) | ANSI/ESDA/JEDEC Standard JS-002 |
| MSL – Moisture Sensitivity<br>Level | MSL    | MSL 1             | IPC/JEDEC Standard J-STD-020    |



## **ROHS COMPLIANCE**

Gallium Semiconductor's Policy on EU RoHS available online:

https://www.galliumsemi.com/\_files/ugd/3748d3\_1107b9788f9845f78f45d424097c4c97.pdf



## 50V,2.4- 2.5GHz, 300W GaN HEMT

#### **REVISION HISTORY**

| Revision | Date       | Datasheet Status | Modifications             |
|----------|------------|------------------|---------------------------|
| А        | 04/20/2023 | Advanced         | Init                      |
| В        | 08/18/2023 | Advanced         | Updated Rth and Test Data |

#### **CONTACT INFORMATION**

To request latest information and samples, please contact us at:

Web: <a href="https://www.galliumsemi.com/">https://www.galliumsemi.com/</a>

Email: sales@galliumsemi.com

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